

Product Summary

| BV_{DSS} | $R_{DS(on)}$ | I_D $T_A = +25^\circ C$ |
|------------|----------------------------------|------------------------------|
| -60V | 125m Ω @ $V_{GS} = -10V$ | -4.3A |
| | 190m Ω @ $V_{GS} = -4.5V$ | -3.5A |

Description

This MOSFET is designed to minimize the on-state resistance and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Motor Control
- DC-DC Converters
- Power Management Functions
- Uninterrupted Power Supply

Features and Benefits

- Fast Switching Speed
- Low Gate Drive
- Low Input Capacitance
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

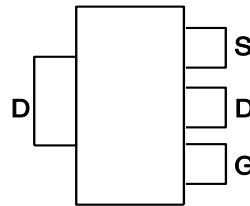
Mechanical Data

- Case: SOT223
- Case Material: Molded Plastic, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.112 grams (Approximate)

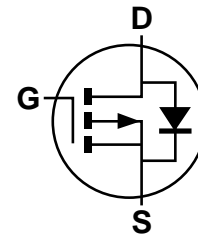
SOT223



Top View



Pin Out - Top View



Equivalent Circuit

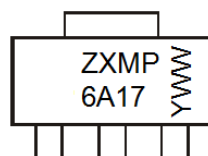
Ordering Information (Note 4)

| Part Number | Case | Packaging |
|-------------|--------|--------------------|
| ZXMP6A17GTA | SOT223 | 1,000/Tape & Reel |
| ZXMP6A17GTC | SOT223 | 4,000/ Tape & Reel |

- Note:
1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information

SOT223



ZXMP6A17 = Product Type Marking Code
 YWW = Date Code Marking
 Y or \bar{Y} = Year (ex: 5 = 2015)
 WW or $\bar{W}W$ = Week (01 - 53)

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

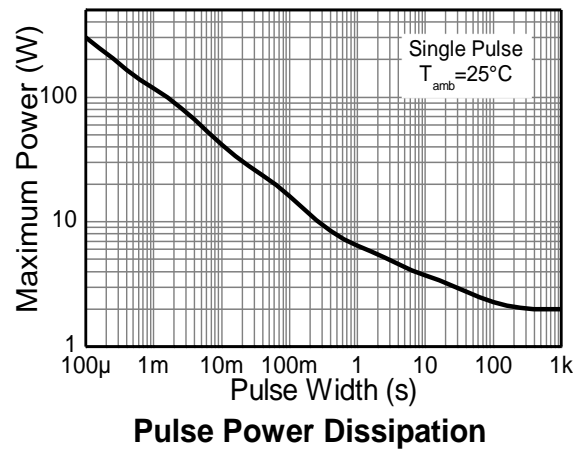
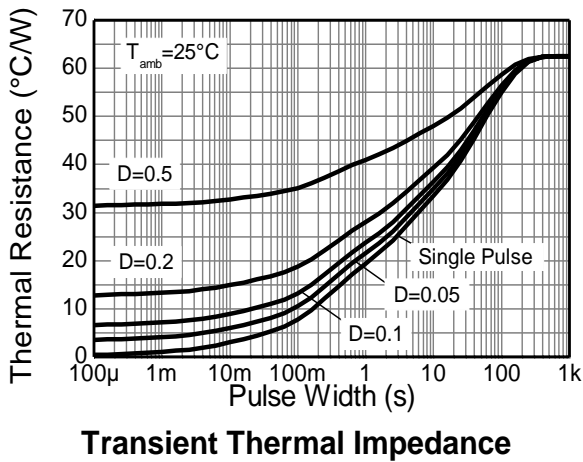
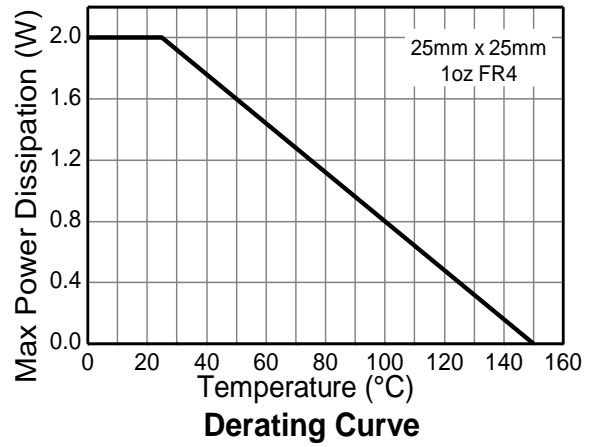
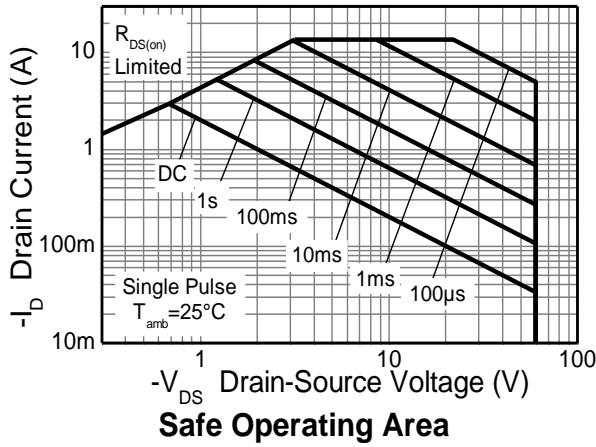
| Characteristic | | | Symbol | Value | Unit | |
|--|-----------------------|------------------------------------|-----------|----------|-------|---|
| Drain-Source Voltage | | | V_{DSS} | -60 | V | |
| Gate-Source Voltage | | | V_{GSS} | ± 20 | V | |
| Continuous Drain Current | $V_{GS} = 10\text{V}$ | (Note 6) | I_D | -4.3 | A | |
| | | $T_A = +70^\circ\text{C}$ (Note 6) | | -3.5 | | |
| | | (Note 5) | | -3.0 | | |
| Pulsed Drain Current | $V_{GS} = 10\text{V}$ | (Note 7) | I_{DM} | -13.7 | A | |
| Continuous Source Current (Body Diode) | | | (Note 6) | I_S | -4.8 | A |
| Pulsed Source Current (Body Diode) | | | (Note 7) | I_{SM} | -13.7 | A |

Thermal Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

| Characteristic | | Symbol | Value | Unit |
|---|------------------------|-----------------|-------------|----------------------|
| Power Dissipation | (Note 5) | P_D | 2.0 | W |
| | Linear Derating Factor | | 16 | |
| | (Note 6) | | 3.9 | mW/ $^\circ\text{C}$ |
| | | | 31 | |
| Thermal Resistance, Junction to Ambient | (Note 5) | $R_{\theta JA}$ | 62.5 | $^\circ\text{C/W}$ |
| | (Note 6) | | 32.0 | |
| Thermal Resistance, Junction to Lead | (Note 8) | $R_{\theta JL}$ | 9.8 | |
| Operating and Storage Temperature Range | | T_J, T_{STG} | -55 to +150 | $^\circ\text{C}$ |

- Notes:
5. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
 6. Same as Note 5, except the device is measured at $t \leq 10\text{sec}$.
 7. Same as Note 5, except the device is pulsed with $D = 0.02$ and pulse width 300 μs . The pulse current is limited by the maximum junction temperature.
 8. Thermal resistance from junction to solder-point (at the end of the drain lead).

Thermal Characteristics

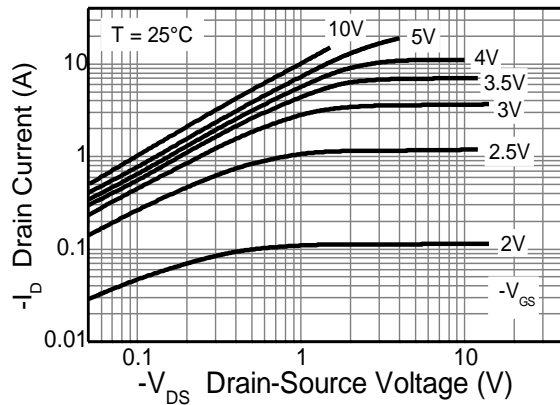


Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

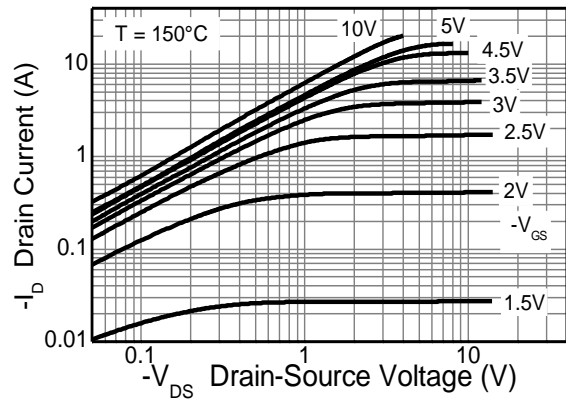
| Characteristic | Symbol | Min | Typ | Max | Unit | Test Condition |
|--|---------------------|------|-------|-------|------|---|
| OFF CHARACTERISTICS | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | -60 | — | — | V | I _D = -250μA, V _{GS} = 0V |
| Zero Gate Voltage Drain Current | I _{DSS} | — | — | -0.5 | μA | V _{DS} = -60V, V _{GS} = 0V |
| Gate-Source Leakage | I _{GSS} | — | — | ±100 | nA | V _{GS} = ±20V, V _{DS} = 0V |
| ON CHARACTERISTICS | | | | | | |
| Gate Threshold Voltage | V _{GS(th)} | -1.0 | — | — | V | I _D = -250μA, V _{DS} = V _{GS} |
| Static Drain-Source On-Resistance (Note 9) | R _{DS(on)} | — | 0.096 | 0.125 | Ω | V _{GS} = -10V, I _D = -2.2A |
| | | | 0.120 | 0.190 | | V _{GS} = -4.5V, I _D = -1.8A |
| Forward Transconductance (Notes 9 & 10) | g _{fs} | — | 4.7 | — | S | V _{DS} = -15V, I _D = -2.2A |
| Diode Forward Voltage (Note 9) | V _{SD} | — | -0.85 | -0.95 | V | I _S = -2.0A, V _{GS} = 0V, T _J = +25°C |
| Reverse Recovery Time (Note 10) | t _{rr} | — | 25.1 | — | ns | I _S = -1.7A, di/dt = 100A/μs, |
| Reverse Recovery Charge (Note 10) | Q _{rr} | — | 27.2 | — | nC | T _J = +25°C |
| DYNAMIC CHARACTERISTICS (Note 10) | | | | | | |
| Input Capacitance | C _{iss} | — | 637 | — | pF | V _{DS} = -30V, V _{GS} = 0V f = 1MHz |
| Output Capacitance | C _{oss} | — | 70.0 | — | pF | |
| Reverse Transfer Capacitance | C _{rss} | — | 53.0 | — | pF | |
| Total Gate Charge (Note 11) | Q _g | — | 9.0 | — | nC | V _{GS} = -4.5V |
| Total Gate Charge (Note 11) | Q _g | — | 17.7 | — | nC | V _{GS} = -10V V _{DS} = -30V I _D = -2.2A |
| Gate-Source Charge (Note 11) | Q _{gs} | — | 1.6 | — | nC | |
| Gate-Drain Charge (Note 11) | Q _{gd} | — | 4.4 | — | nC | |
| Turn-On Delay Time (Note 11) | t _{D(on)} | — | 2.6 | — | ns | V _{DD} = -30V, V _{GS} = -10V I _D = -1A, R _G ≅ 6.0Ω |
| Turn-On Rise Time (Note 11) | t _r | — | 3.4 | — | ns | |
| Turn-Off Delay Time (Note 11) | t _{D(off)} | — | 26.2 | — | ns | |
| Turn-Off Fall Time (Note 11) | t _f | — | 11.3 | — | ns | |

- Notes:
9. Measured under pulsed conditions. Pulse width ≤ 300μs; duty cycle ≤ 2%.
 10. For design aid only, not subject to production testing.
 11. Switching characteristics are independent of operating junction temperatures.

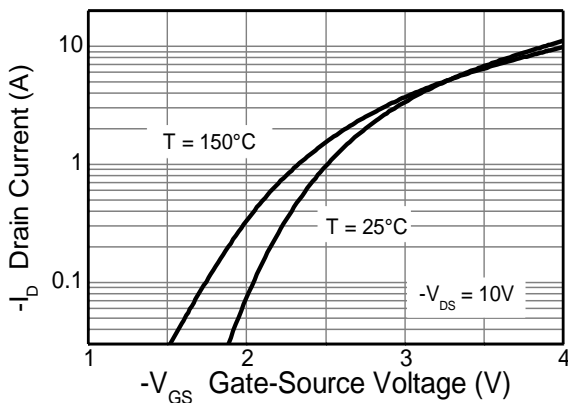
Typical Characteristics



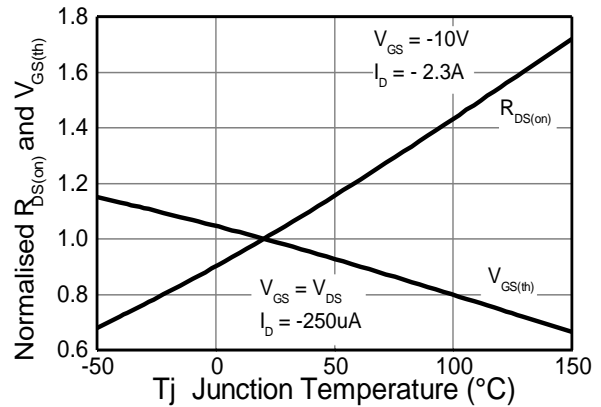
Output Characteristics



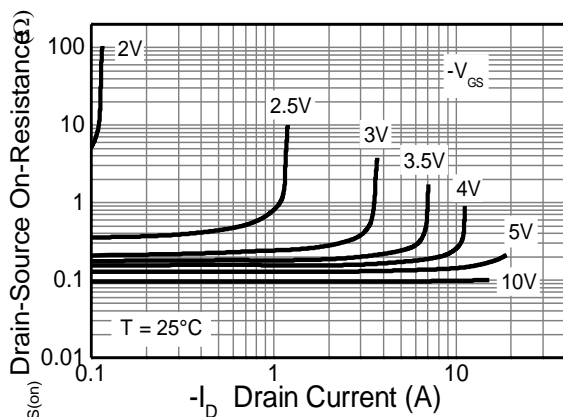
Output Characteristics



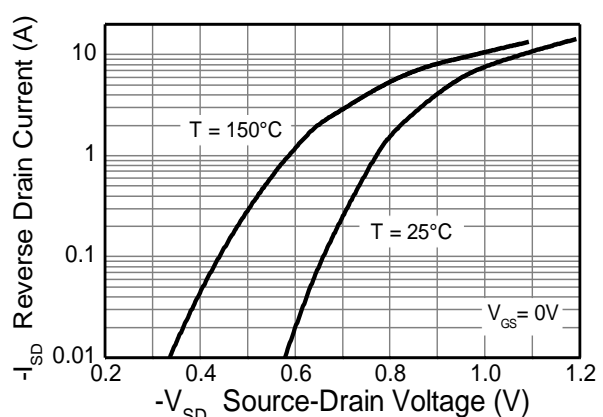
Typical Transfer Characteristics



Normalised Curves v Temperature

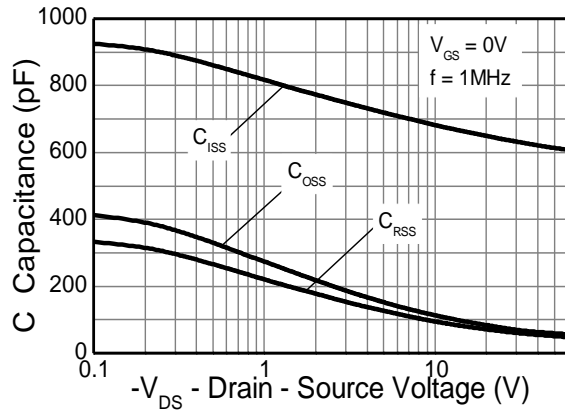


On-Resistance v Drain Current

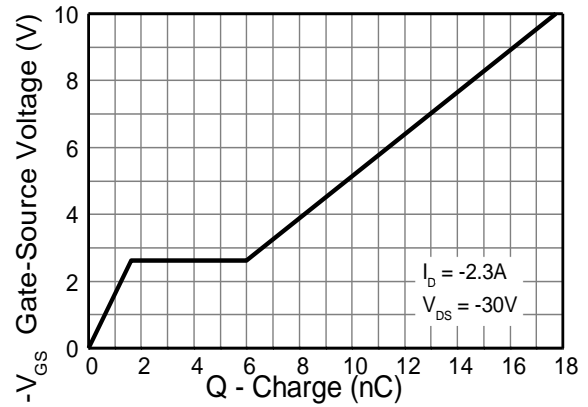


Source-Drain Diode Forward Voltage

Typical Characteristics (cont.)

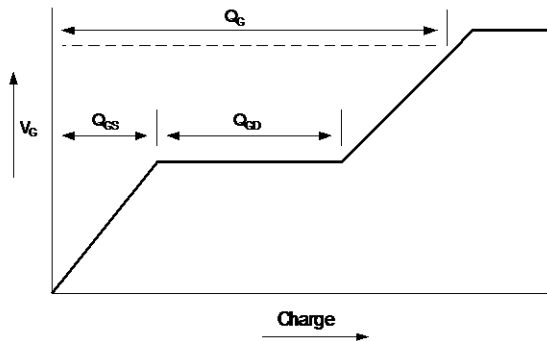


Capacitance v Drain-Source Voltage

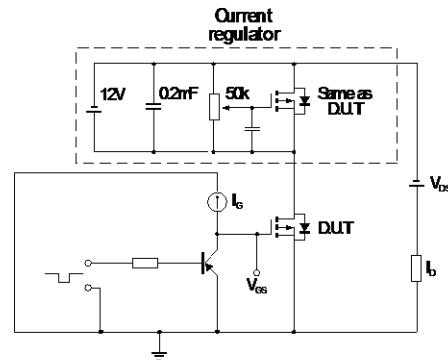


Gate-Source Voltage v Gate Charge

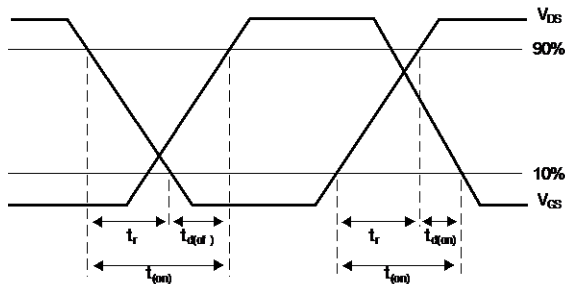
Test Circuits



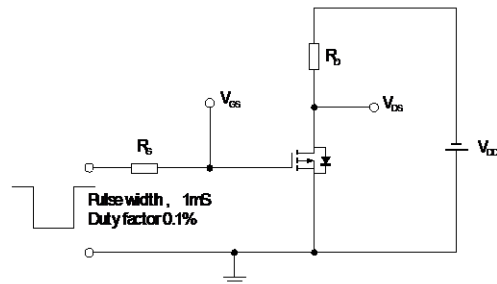
Basic gate charge waveform



Gate charge test circuit



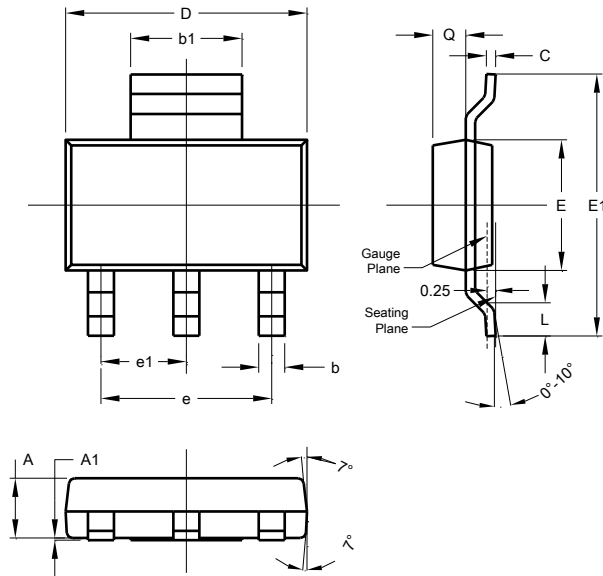
Switching time waveforms



Switching time test circuit

Package Outline Dimensions

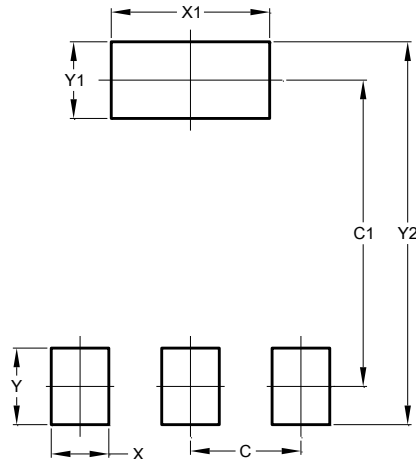
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.



| SOT223 | | | |
|-----------------------------|-------|------|------|
| Dim | Min | Max | Typ |
| A | 1.55 | 1.65 | 1.60 |
| A1 | 0.010 | 0.15 | 0.05 |
| b | 0.60 | 0.80 | 0.70 |
| b1 | 2.90 | 3.10 | 3.00 |
| C | 0.20 | 0.30 | 0.25 |
| D | 6.45 | 6.55 | 6.50 |
| E | 3.45 | 3.55 | 3.50 |
| E1 | 6.90 | 7.10 | 7.00 |
| e | - | - | 4.60 |
| e1 | - | - | 2.30 |
| L | 0.85 | 1.05 | 0.95 |
| Q | 0.84 | 0.94 | 0.89 |
| All Dimensions in mm | | | |

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



| Dimensions | Value (in mm) |
|------------|---------------|
| C | 2.30 |
| C1 | 6.40 |
| X | 1.20 |
| X1 | 3.30 |
| Y | 1.60 |
| Y1 | 1.60 |
| Y2 | 8.00 |

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